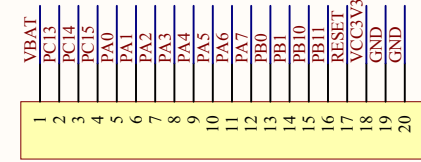
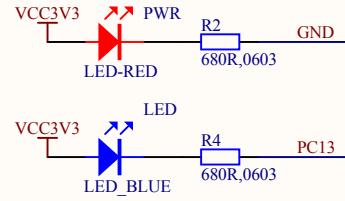


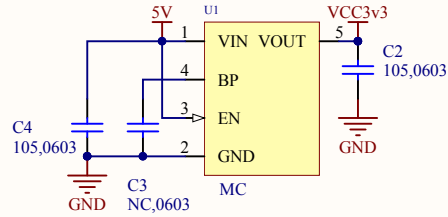
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X	0	FLASH
1	1	SRAM
0	1	ISP



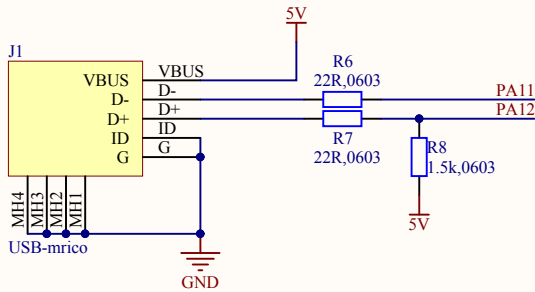
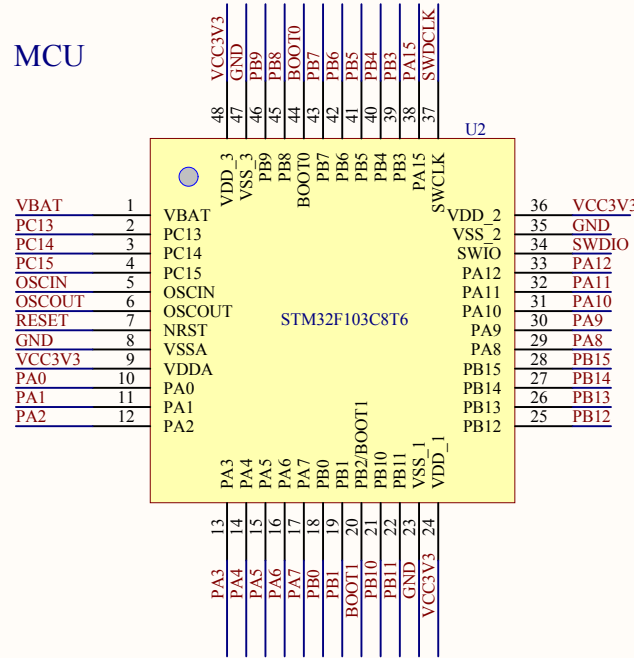
P1
Header 20



P2
Header 20

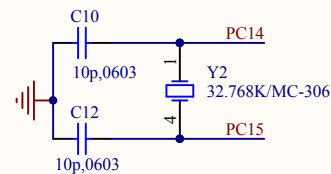
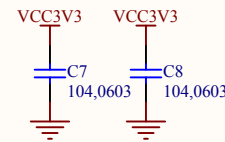
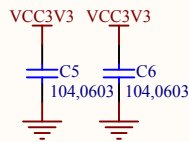
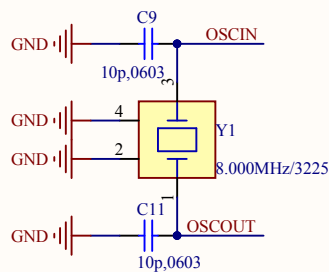
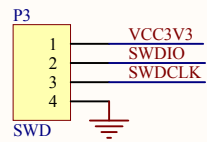
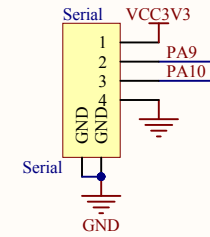


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Title		
STM32F103C8T6核心板原理图		
Size	Number	Revision
A4	源地工作室www.vcc-gnd.com	
Date:	2019/1/2	Sheet of
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