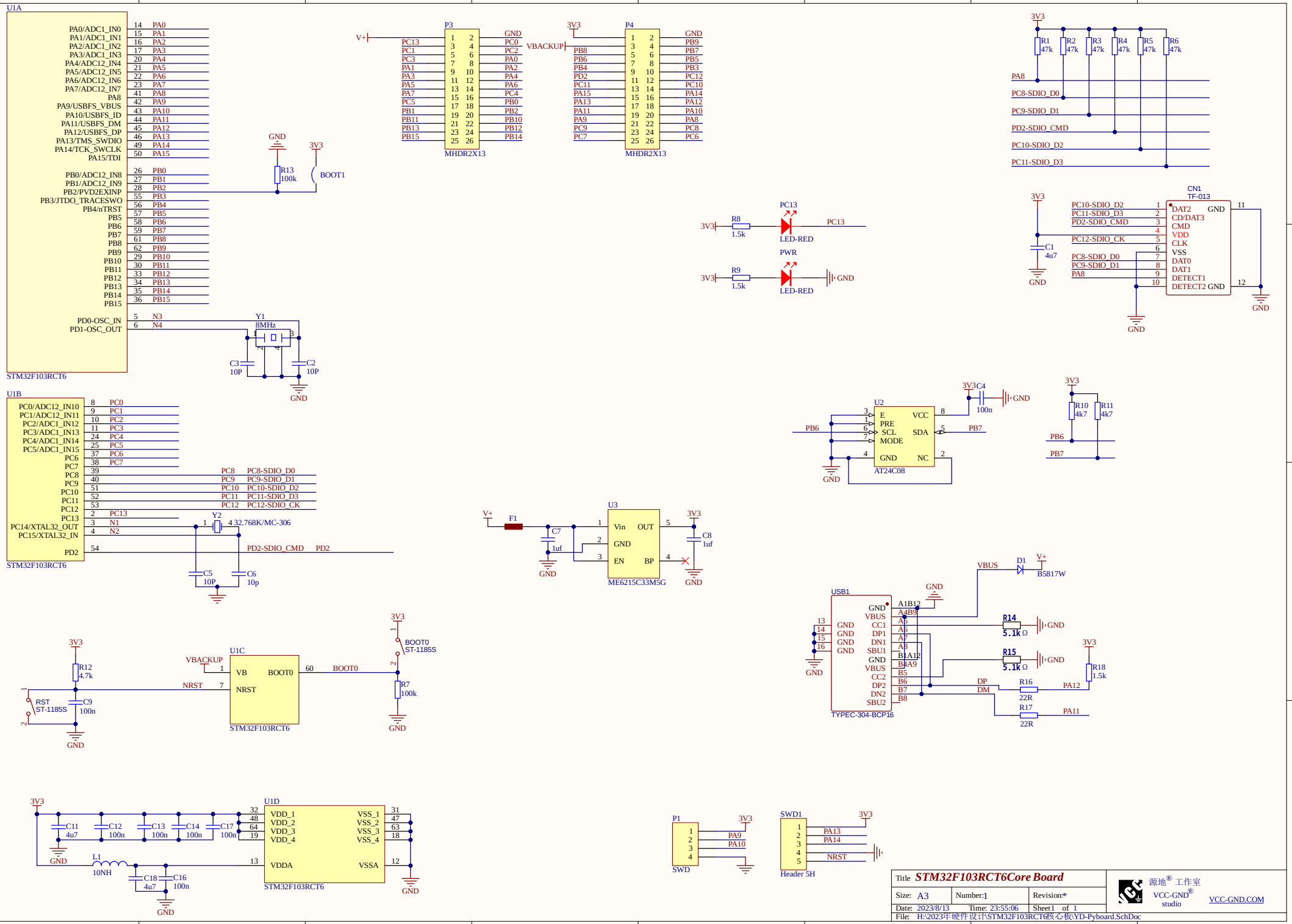
U1A
STM32F103RCT6
PA0/ADC1_IN0
PA1/ADC1_IN1
PA2/ADC1_IN2
PA3/ADC1_IN3
PA4/ADC12_IN4
PA5/ADC12_IN5
PA6/ADC12_IN6
PA7/ADC12_IN7
PA8
PA9/USBFS_VBUS
PA10/USBFS_ID
PA11/USBFS_DM
PA12/USBFS_DP
PA13/TMS_SWDIO
PA14/TCK_SWCLK
PA15/TDI
PB0/ADC12_IN8
PB1/ADC12_IN9
PB2/PVD2EXINP
PB3/JTDO_TRACESWO
PB4/nTRST
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
PD0-OSC_IN
PD1-OSC_OUT
GND
R13
100k
3V3
BOOT1
Y1
8MHz
C3
10P
C2
10P
U1B
STM32F103RCT6
PC0/ADC12_IN10
PC1/ADC12_IN11
PC2/ADC1_IN12
PC3/ADC1_IN13
PC4/ADC1_IN14
PC5/ADC1_IN15
PC6
PC7
PC8
PC9
PC10
PC11
PC12
PC13
PC14/XTAL32_OUT
PC15/XTAL32_IN
PD2
PC8-SDIO_D0
PC9-SDIO_D1
PC10-SDIO_D2
PC11-SDIO_D3
PC12-SDIO_CK
Y2
32.768K/MC-306
C5
10P
C6
10p
PD2-SDIO_CMD
P3
MHDR2X13
V+
VBACKUP
P4
MHDR2X13
R1
47k
R2
47k
R3
47k
R4
47k
R5
47k
R6
47k
PC8-SDIO_D0
PC9-SDIO_D1
PD2-SDIO_CMD
PC10-SDIO_D2
PC11-SDIO_D3
R8
1.5k
LED-RED
R9
1.5k
PWR
CN1
TF-013
DAT2
CD/DAT3
CMD
VDD
CLK
VSS
DAT0
DAT1
DETECT1
DETECT2
C1
4u7
U2
AT24C08
E
PRE
SCL
MODE
VCC
SDA
GND
NC
C4
100n
R10
4k7
R11
4k7
U3
ME6215C33M5G
Vin
OUT
GND
EN
BP
F1
C7
1uf
C8
1uf
R12
4.7k
RST
ST-1185S
C9
100n
U1C
STM32F103RCT6
VB
BOOT0
NRST
BOOT0
ST-1185S
R7
100k
USB1
TYPEC-304-BCP16
VBUS
D1
B5817W
R14
5.1kΩ
R15
5.1kΩ
R16
22R
R17
22R
R18
1.5k
DP
DM
PA12
PA11
U1D
STM32F103RCT6
VDD_1
VDD_2
VDD_3
VDD_4
VSS_1
VSS_2
VSS_3
VSS_4
VDDA
VSSA
C11
4u7
C12
100n
C13
100n
C14
100n
C17
100n
L1
10NH
C18
4u7
C16
100n
P1
SWD
PA9
PA10
SWD1
Header 5H
PA13
PA14
NRST
Title STM32F103RCT6Core Board
Size: A3
Number:1
Revision:*
Date: 2023/8/13
Time: 23:55:06
Sheet1 of 1
File: H:\2023年硬件设计\STM32F103RCT6核心板\YD-Pyboard.SchDoc
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