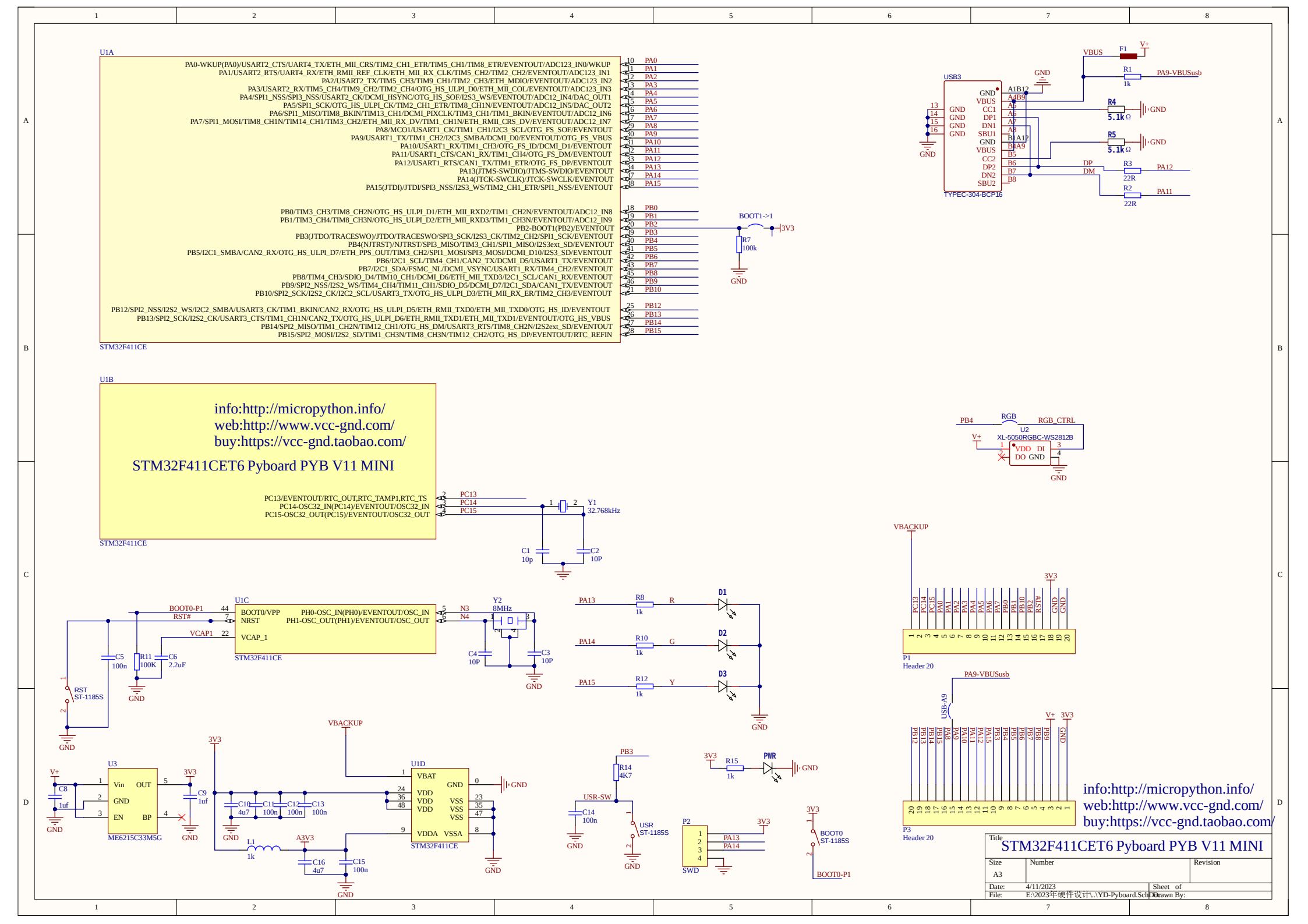
U1A
PA0-WKUP(PA0)/USART2_CTS/UART4_TX/ETH_MII_CRS/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/EVENTOUT/ADC123_IN0/WKUP
PA1/USART2_RTS/UART4_RX/ETH_RMII_REF_CLK/ETH_MII_RX_CLK/TIM5_CH2/TIM2_CH2/EVENTOUT/ADC123_IN1
PA2/USART2_TX/TIM5_CH3/TIM9_CH1/TIM2_CH3/ETH_MDIO/EVENTOUT/ADC123_IN2
PA3/USART2_RX/TIM5_CH4/TIM9_CH2/TIM2_CH4/OTG_HS_ULPI_D0/ETH_MII_COL/EVENTOUT/ADC123_IN3
PA4/SPI1_NSS/SPI3_NSS/USART2_CK/DCMI_HSYNC/OTG_HS_SOF/I2S3_WS/EVENTOUT/ADC12_IN4/DAC_OUT1
PA5/SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM8_CH1N/EVENTOUT/ADC12_IN5/DAC_OUT2
PA6/SPI1_MISO/TIM8_BKIN/TIM13_CH1/DCMI_PIXCLK/TIM3_CH1/TIM1_BKIN/EVENTOUT/ADC12_IN6
PA7/SPI1_MOSI/TIM8_CH1N/TIM14_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/ETH_RMII_CRS_DV/EVENTOUT/ADC12_IN7
PA8/MCO1/USART1_CK/TIM1_CH1/I2C3_SCL/OTG_FS_SOF/EVENTOUT
PA9/USART1_TX/TIM1_CH2/I2C3_SMBA/DCMI_D0/EVENTOUT/OTG_FS_VBUS
PA10/USART1_RX/TIM1_CH3/OTG_FS_ID/DCMI_D1/EVENTOUT
PA11/USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM/EVENTOUT
PA12/USART1_RTS/CAN1_TX/TIM1_ETR/OTG_FS_DP/EVENTOUT
PA13(JTMS-SWDIO)/JTMS-SWDIO/EVENTOUT
PA14(JTCK-SWCLK)/JTCK-SWCLK/EVENTOUT
PA15(JTDI)/JTDI/SPI3_NSS/I2S3_WS/TIM2_CH1_ETR/SPI1_NSS/EVENTOUT
PB0/TIM3_CH3/TIM8_CH2N/OTG_HS_ULPI_D1/ETH_MII_RXD2/TIM1_CH2N/EVENTOUT/ADC12_IN8
PB1/TIM3_CH4/TIM8_CH3N/OTG_HS_ULPI_D2/ETH_MII_RXD3/TIM1_CH3N/EVENTOUT/ADC12_IN9
PB2-BOOT1(PB2)/EVENTOUT
PB3(JTDO/TRACESWO)/JTDO/TRACESWO/SPI3_SCK/I2S3_CK/TIM2_CH2/SPI1_SCK/EVENTOUT
PB4(NJTRST)/NJTRST/SPI3_MISO/TIM3_CH1/SPI1_MISO/I2S3ext_SD/EVENTOUT
PB5/I2C1_SMBA/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/TIM3_CH2/SPI1_MOSI/SPI3_MOSI/DCMI_D10/I2S3_SD/EVENTOUT
PB6/I2C1_SCL/TIM4_CH1/CAN2_TX/DCMI_D5/USART1_TX/EVENTOUT
PB7/I2C1_SDA/FSMC_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2/EVENTOUT
PB8/TIM4_CH3/SDIO_D4/TIM10_CH1/DCMI_D6/ETH_MII_TXD3/I2C1_SCL/CAN1_RX/EVENTOUT
PB9/SPI2_NSS/I2S2_WS/TIM4_CH4/TIM11_CH1/SDIO_D5/DCMI_D7/I2C1_SDA/CAN1_TX/EVENTOUT
PB10/SPI2_SCK/I2S2_CK/I2C2_SCL/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/TIM2_CH3/EVENTOUT
PB12/SPI2_NSS/I2S2_WS/I2C2_SMBA/USART3_CK/TIM1_BKIN/CAN2_RX/OTG_HS_ULPI_D5/ETH_RMII_TXD0/ETH_MII_TXD0/OTG_HS_ID/EVENTOUT
PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N/CAN2_TX/OTG_HS_ULPI_D6/ETH_RMII_TXD1/ETH_MII_TXD1/EVENTOUT/OTG_HS_VBUS
PB14/SPI2_MISO/TIM1_CH2N/TIM12_CH1/OTG_HS_DM/USART3_RTS/TIM8_CH2N/I2S2ext_SD/EVENTOUT
PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N/TIM8_CH3N/TIM12_CH2/OTG_HS_DP/EVENTOUT/RTC_REFIN
STM32F411CE
BOOT1->1
3V3
R7
100k
GND
U1B
info:http://micropython.info/
web:http://www.vcc-gnd.com/
buy:https://vcc-gnd.taobao.com/
STM32F411CET6 Pyboard PYB V11 MINI
PC13/EVENTOUT/RTC_OUT,RTC_TAMP1,RTC_TS
PC14-OSC32_IN(PC14)/EVENTOUT/OSC32_IN
PC15-OSC32_OUT(PC15)/EVENTOUT/OSC32_OUT
STM32F411CE
Y1
32.768kHz
C1
10p
C2
10P
USB3
TYPEC-304-BCP16
VBUS
F1
V+
R1
1k
PA9-VBUSusb
R4
5.1k Ω
R5
5.1k Ω
R3
22R
R2
22R
PA12
PA11
DP
DM
U2
XL-5050RGBC-WS2812B
PB4
RGB
RGB_CTRL
U1C
BOOT0-P1
RST#
VCAP1
BOOT0/VPP
NRST
VCAP_1
PH0-OSC_IN(PH0)/EVENTOUT/OSC_IN
PH1-OSC_OUT(PH1)/EVENTOUT/OSC_OUT
STM32F411CE
Y2
8MHz
C5
100n
R11
100K
C6
2.2uF
C4
10P
C3
10P
RST
ST-1185S
PA13
PA14
PA15
R8
R10
R12
1k
D1
D2
D3
VBACKUP
P1
Header 20
P3
Header 20
U3
ME6215C33M5G
C8
1uf
C9
1uf
U1D
STM32F411CE
C10
4u7
C11
100n
C12
100n
C13
100n
C16
4u7
C15
100n
L1
1k
A3V3
PB3
R14
4K7
USR-SW
C14
100n
USR
ST-1185S
R15
1k
PWR
P2
SWD
BOOT0
ST-1185S
BOOT0-P1
USB-A9
info:http://micropython.info/
web:http://www.vcc-gnd.com/
buy:https://vcc-gnd.taobao.com/
Title
STM32F411CET6 Pyboard PYB V11 MINI
Size
A3
Number
Revision
Date:
4/11/2023
Sheet of
File:
E:\2023年硬件设计\..\YD-Pyboard.SchDoc
Drawn By: